## NAME: RAJESH M S



## **DESIGNATION:**Assistant Professor in Electronics Engg.

Department of Electronics & Communication Engineering College of Engineering, Adoor Manakkala – P.O, Pathanamthitta. Kerala Pin-691551

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Name	RAJESH M S
Designation	Assistant Professor in Electronics Engineering
Date of Birth	02-04-1972
Sex	Male
Marital Status	Married
Name of Father	K.Sreedhara Hegdan
Name of Mother	V.Jankibai
E-mail	rajeshms@cea.ac.in
Mobile Number	9447112179
Official Address	College of Engineering Adoor
Home Address	Madathilparambil, Karuvatta, Adoor, PIN-691523, Pathanamthitta

EDUCATIONAL QUALIFICATIONS					
Degree	University	Year of	Specialization	Percentage %	
		Passing		Marks/CGPA	
В ТЕСН	CUSAT	1995	ELECTRONICS Engg.	FIRST CLASS	
M TECH	NITK SURATHKAL	2013	VLSI	FIRST CLASS	
<b>ADDITIONA</b>	L QUALIFICATIONS				

EXPERIENCE- 21 years of experience						
<b>Oraganization</b>	<b>Position</b>	Date Experience			ience	
		From	То	Years	Months	
College of Engineering	Assistant Professor	06.09.2001	12.06.2018	16 Years	9months	

Adoor					
College of Engineering	Assistant Professor	13.06.2018	10.08.2022	4 Years	2months
Chengannur					
College of Engineering	Assistant Professor	11.08.2022	Till date		6 months
Adoor					

CONFERENCES ATTENDED					
SI no	Conference	Organized By	Date	Year	
	17 <sup>th</sup> National Conference on	College of Engineering			
1	Technological Trends (NCTT 2016)	Trivandrum	19 – 20 August	2016	
	National Conference on Power,				
	Control, Communication and				
2	Computing(NPCCC'17)	College of Engineering Adoor	1 – 3 February	2017	

FDP/	FDP/STTP/SEMINAR/WORKSHOPS DETAILS						
SI No	Course Title	Organized By	Duration	Year	Date		
	FDP on Research Trends in						
	Semiconductor Modeling						
1	and Fabrication	College of Engineering Cherthala	5days	2014	8 - 12 December		
	STTP on Advances in	TKM College of Engineering					
2	Nanotechnology	Kollam	1 week	2015	23-28 March		
	National Workshop on						
	Advanced Nano-Scale	College of Engineering					
3	Device Design Using TCAD	Chengannur	5days	2016	28 Dec -1 Jan		
	FDP on Research Trends in		_				
4	Digital Image Processing	College of Engineering Adoor	3days	2016	6 - 8 January		
	FDP on Mathematical						
5	Applications in Engineering	College of Engineering Adoor	3days	2016	29 - 31 March		
	FDP on Semiconductor						
	Devices, Circuits and						
_	Systems with a research	uupp.	4144	204.6	4 0 1 1		
6	perspective	IHRD	1Week	2016	4 - 9 July		
	FDP on Power System,						
_	Drives and Control: Practical	HIDD	Edama	2016	2 70 dahar		
7	Aspects FDP on computational	IHRD	5days	2016	3 - 7 October		
8	Bioinformatics	College of Engineering Adoor	5days	2016	5 - 9 December		
0	FDP on LAMP(Linux, Apache,	Conege of Engineering Adoor	Juays	2010	3 - 9 December		
9	MySQL, php)	College of Engineering Adoor	3days	2016	13 - 15 December		
	FDP on Advanced Computer	Conege of Engineering Addor	Suays	2010	13 - 13 December		
10	Architecture	College of Engineering Adoor	3days	2016	19 - 21 December		
	Workshop on System Design	conege of Engineering/tagor	Saays	2010	15 ZI December		
11	using LABVIEW	College of Engineering Adoor	5days	2017	16 - 20 January		
	FDP on Programming	22300 2. 2320373001	22.0,0				
	Robots: Sensors, Actuators						
12	and Algorithms	College of Engineering Adoor	5days	2017	6 - 10 February		
	FDP on Roadmap to		· · · · · · · · · · · · · · · · · · ·		,		
	Research in Electrical,						
13	Electronics and Computing	IHRD	5days	2017	13 - 17 February		
	Faculty Development	Teaching Learning Centre IIT					
14	Programme	Madras	3days	2017	6 - 8 March		
	FDP on Emerging Trends in						
15	Computing	College of Engineering Adoor	5days	2017	20 - 24 March		
	FDP on LaTex and Network						
16	Simulation	College of Engineering Adoor	5days	2017	27 - 31 March		
	FDP on Recent Advances in	College of Engineering					
17	sensors and Actuators	Chengannur	12 days	2019	9-20 December		

ORAGANIZER/CONVENOR of International / National Conferences / Symposia / Seminars/Workshops/ FDPs						
SI No	Event	Date	Year	Role		
1	National Conference on Power, Control, Communication & Computing (NPCCC'17) organized by College of Engineering Adoor	01 -03 February	2017	General Editor of Proceedings of NPCCC'17 & Member of the Publication committee		
	Organized a 4 day Workshop on SPICE Simulation &	20 – 23				
2	VHDL at College of Engineering Adoor	February	2017	Co-ordinator		

CO & EXT	CO & EXTRA CURRICULAR					
SI. No.	Member of respective committees / office bearer etc	Year	Achievements/ Awards			
1						
2						
3						
4						
5						

I hereby declare that the above cited information is true to the best of my knowledge and belief.